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**PATENT - POWER OF ATTORNEY
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AND
CHANGE OF CORRESPONDENCE ADDRESS**

Patent Number	7,071,014
Issue Date	7/4/2005
First Named Inventor	Currie et al.
Title	Methods for Preserving Strained Semiconductor Substrate Layers during CMOS Processing
Attorney Docket Number	TSM09-043US-AW

I hereby revoke all previous powers of attorney given in the above-identified patent.

☐ A Power of Attorney is submitted herewith.

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Practitioner(s) Name	Registration Number

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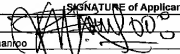
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I am the:

☐ Applicant/Inventor

☒ Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) (Form PTO/SB/06) submitted herewith or filed on _____

SIGNATURE of Applicant or Assignee of Record			
Signature		Date	3/15/2010
Name	Keith Chan	Telephone	
Title and Company	Director of Intellectual Property, Taiwan Semiconductor Manufacturing Company, Ltd.		

NOTE: Signatures of all the Inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.

☒ Total of 1 forms are submitted.

This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.